



<http://iscit2025.org>

## The 24<sup>th</sup> International Symposium on Communications and Information Technologies October 16-18, 2025 – Hanoi, Vietnam

The 24<sup>th</sup> International Symposium on Communications and Information Technologies (ISCIT2025) provides a forum for exchanging ideas, discussing research results, and presenting chip, circuit designs, and applications in communications and information technologies. Advances in circuits and systems enable breakthroughs such as hardware accelerators for AI, brain-machine interfaces, low-power VLSI design, neuromorphic computing, the development of IoT, wearable devices, and 5G-and-6G technologies. The integration of AI into communications and IT brings transformative potential in many areas. We expect this conference to explore and stimulate the contributed research on those subjects. The papers are solicited from prospective authors interested in the related fields.

The ISCIT 2025 conference is organized by the IEEE CAS Vietnam Chapter, co-hosted by the VNU Information Technology Institute (VNU-ITI), and technically supported by the IEEE SSCS Vietnam and the IEICE Vietnam Section. The ISCIT 2025 will be held in Hanoi, Vietnam. Further information about the conference, paper submission guidelines, and templates will be updated on the website.

### Paper Submission

Prospective authors are invited to submit full-length, six-page manuscripts, including figures, tables, and references, to the official ISCIT 2025 website. All papers will be handled electronically. Accepted and presented papers will be included in the conference proceedings with ISBN and **will be submitted for inclusion in IEEE Xplore®** (the copyright shall be assigned to the IEEE). The proceedings of the ISCIT series are regularly indexed by Scopus and listed in the conference proceeding citation index (CPCI) of Clarivate, CORE ranking (rank B). Papers are solicited in the following categories but are not limited to:

- **Circuits and Systems**
  - Analogue and Mixed Signal Processing
  - Brain-machine interface Circuits and Systems
  - Circuits and System for Communications
  - Embedded Communications Systems
  - Hardware Architectures for AI
  - IoT, Wearable Devices and Smart Sensors
  - Low Power Design and VLSI Physical Synthesis
  - Neuromorphic computing
  - 5G and Beyond Technologies
- **Wireless Communications**
  - Joint Communications and Sensing
  - MIMO, Multi-user MIMO, and Massive MIMO
  - Interference Alignment and Cancellation
  - Heterogeneous and Small-Cell Networks
  - Channel Modeling and Propagation
  - Distributed and Cooperative Communications
  - Smart Antennas and Space-Time Processing
  - Communications Security
  - Space Communications
  - Reconfigurable Intelligent Surfaces
  - URLLC
- **Next-Generation Networking**
  - 5G and 6G Networks
- IoT Networking, Data Management, Mining, Fusion and Energy Efficiency
- Cooperative Intelligent Transport Systems
- Networks for Smart Cities
- u-Health Networks
- WLAN, Mesh, and Vehicular Networks
- SDN, NFV, and Cloud-based Networking
- Network Provisioning, Monitoring, and Management
- Emerging Internet Applications
- Energy-Efficient Design and Green Communications
- Network Security and Privacy
- **Artificial Intelligence for Communications and IT**
  - AI-Empowered Communications
  - AI-Enabled Wireless Networking
  - AI-Empowered Mobile Applications
  - Fuzzy Logic and Neural Networks
  - General Machine Learning
  - Deep Learning
  - Stactistic and Probabilistic Methods
  - Knowledge-Based Engineering
  - Big Data Analysis
  - Image, Video, and Speech Processing
  - Natural Language Processing
  - Biomedical Information Analysis

### Important dates

- Full manuscript submission: **July 1, 2025** Link: <https://edas.info/newPaper.php?c=33741>
- Notification of acceptance: September 1, 2025 Contact E-mail: [anhntiti@vnu.edu.vn](mailto:anhntiti@vnu.edu.vn)
- Camera-ready submission: September 15, 2025

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